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DOCUMENT(S) FAXED: (MARKED WITH X) 2 PAGES ATTACHED

Re Applic of	Pogge, et al.
Docket No.	FIS920020007US2
Serial No.	10/709,946
Filing Date	6/08/04
Attorney	LISA U. JAKLITSCH

Document(s) Attached: - **RESPONSE TO RESTRICTION REQUIREMENT-2**  
pages

## PLEASE DELIVER TO:

EXAMINER: NGUYEN, DILINH P.  
ART UNIT: 2814  
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10/709,946

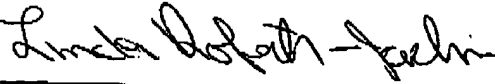
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In the United States Patent and Trademark Office

**Date:** 12/02/05  
**In re Application of:** Pogge, et al.  
**For:** INTEGRATED ELECTRONIC CHIP AND INTERCONNECT DEVICE AND PROCESS FOR MAKING THE SAME  
**Serial Number:** 10/709,946  
**Art Unit:** 2814

**Filed:** 6/08/04  
**Confirmation No.** 3945  
**Examiner:** NGUYEN, DILINH P.

**RESPONSE TO RESTRICTION REQUIREMENT**

Hon. Commissioner of Patents and Trademarks  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In an Office Action mailed November 3, 2005, the Examiner has required restriction to one of the following inventions under 35 USC §121:

- I. Embodiment 1: figs. 2A-5; and
- II. Embodiment 2; figs. 6A-6F.

Applicants hereby elect Embodiment 2; figs. 6A-6F and claims 21-26 for prosecution in this application.

10/709,946

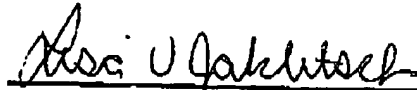
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FIS920020007US2

Further action with respect to the present application is earnestly solicited.

Respectfully Submitted,

Pogge, et al.



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